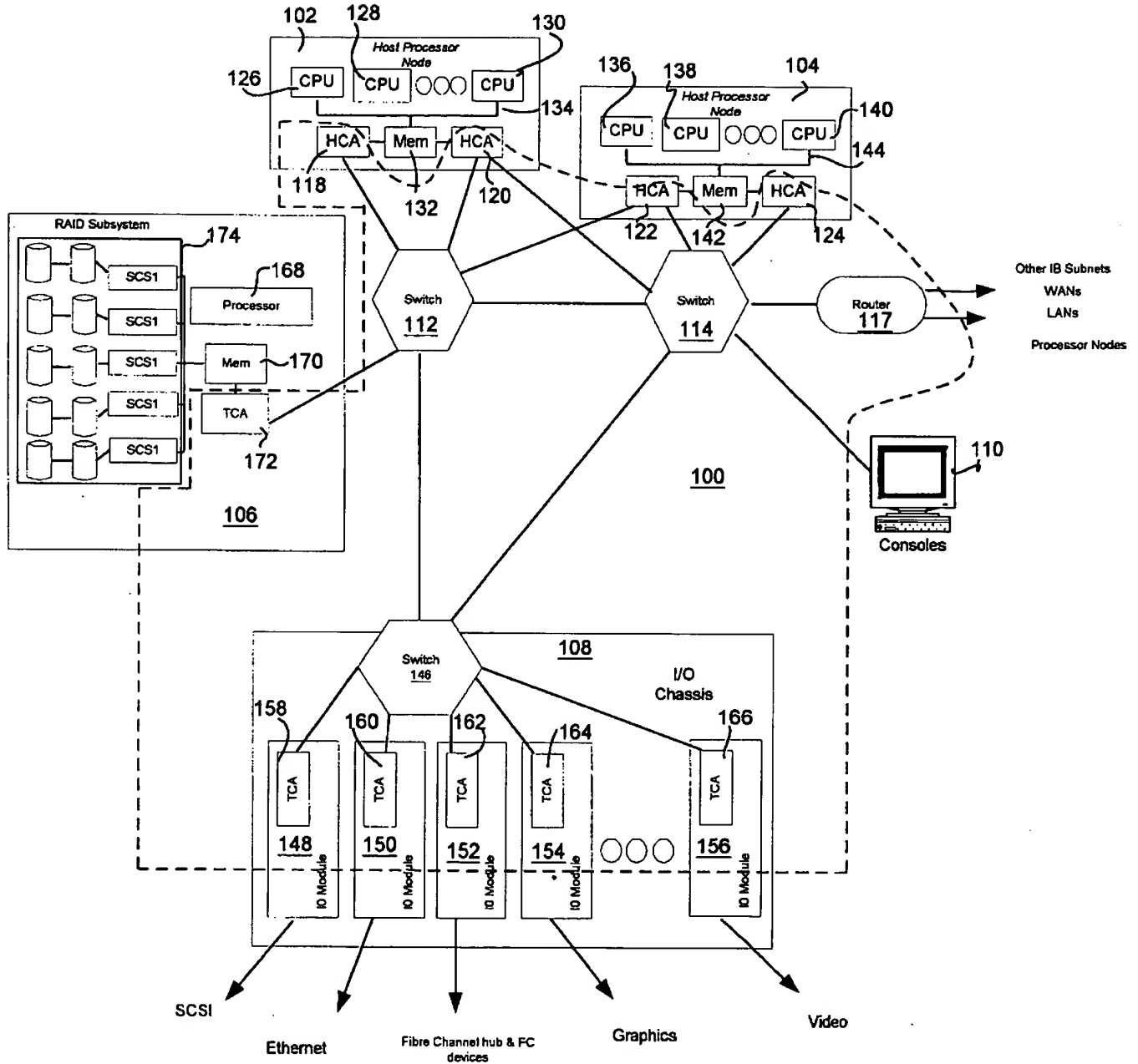


Figure 1

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The diagram illustrates the architecture of a Host Processor Node 200. At the top, a horizontal bar represents the **Host Processor Node**, containing four **Consumer** components: **Consumer 202**, **Consumer 204**, **Consumer 206**, and **Consumer 208**. Below this bar is a large rounded rectangle labeled **Message & Data Service 224**. A dashed line labeled **Verbs** separates the top section from the bottom section. The bottom section contains two **Channel Adapter (endnode)** components: **210** on the left and **212** on the right. Each channel adapter is connected to two **Port** components: **Port 214** and **Port 216** for adapter 210, and **Port 218** and **Port 220** for adapter 212. The entire system is labeled **200** at the bottom center.

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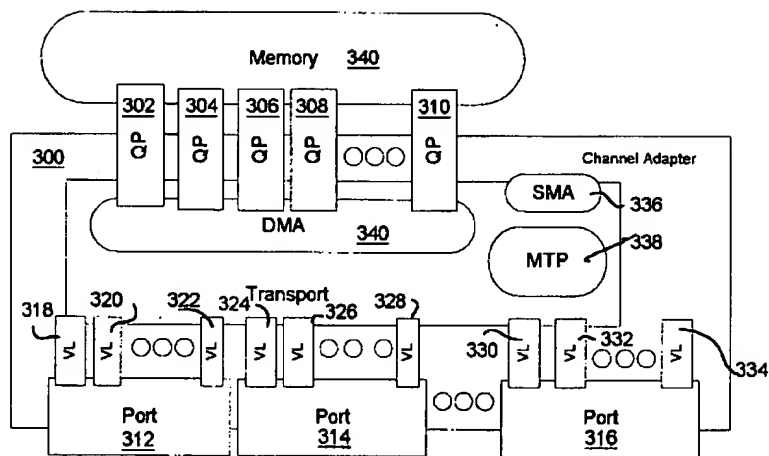


Figure 3

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Figure 4

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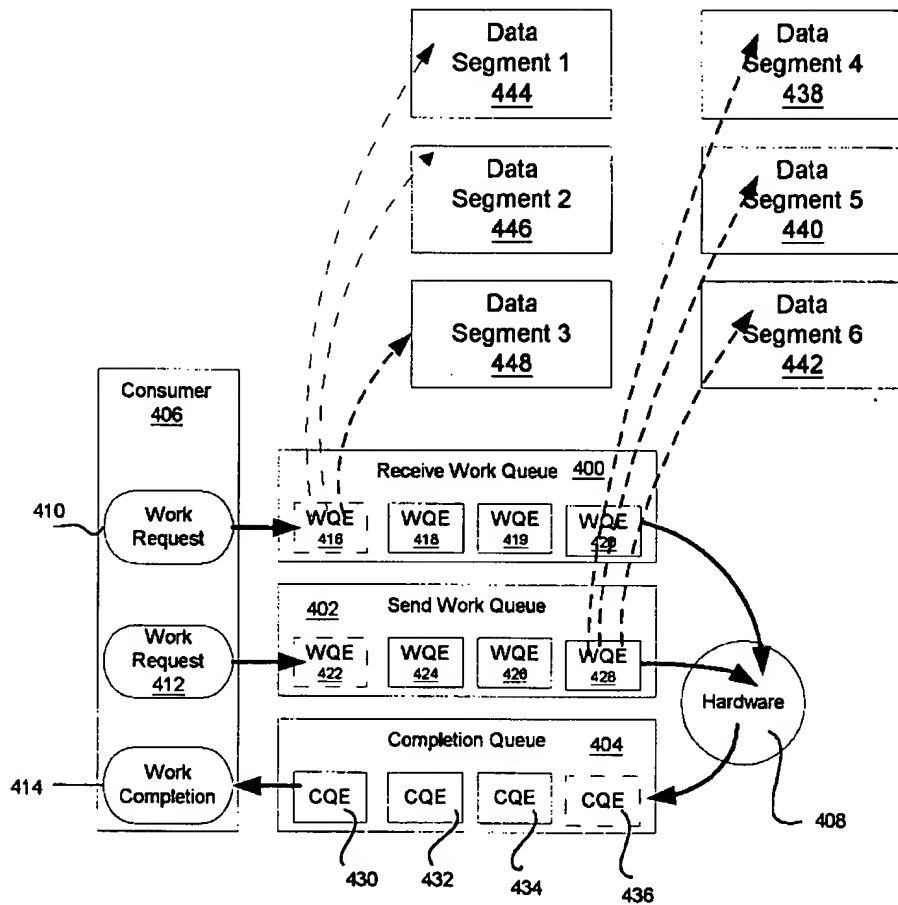


Figure 5

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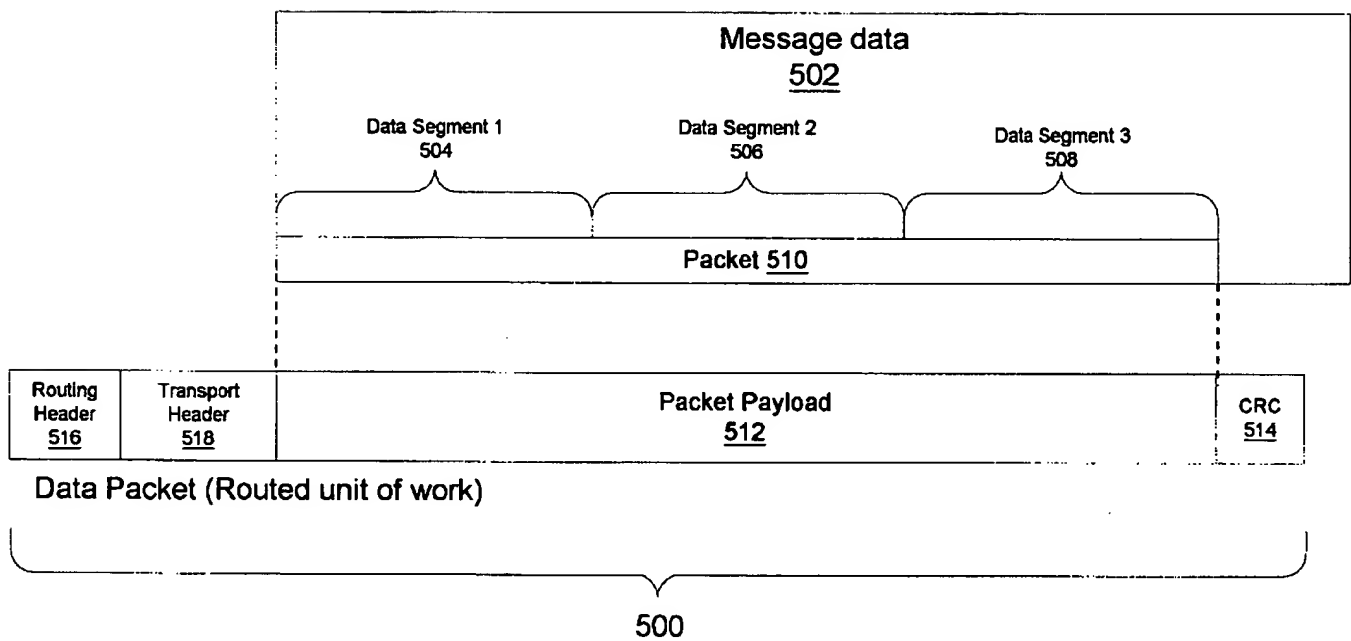


Figure 6

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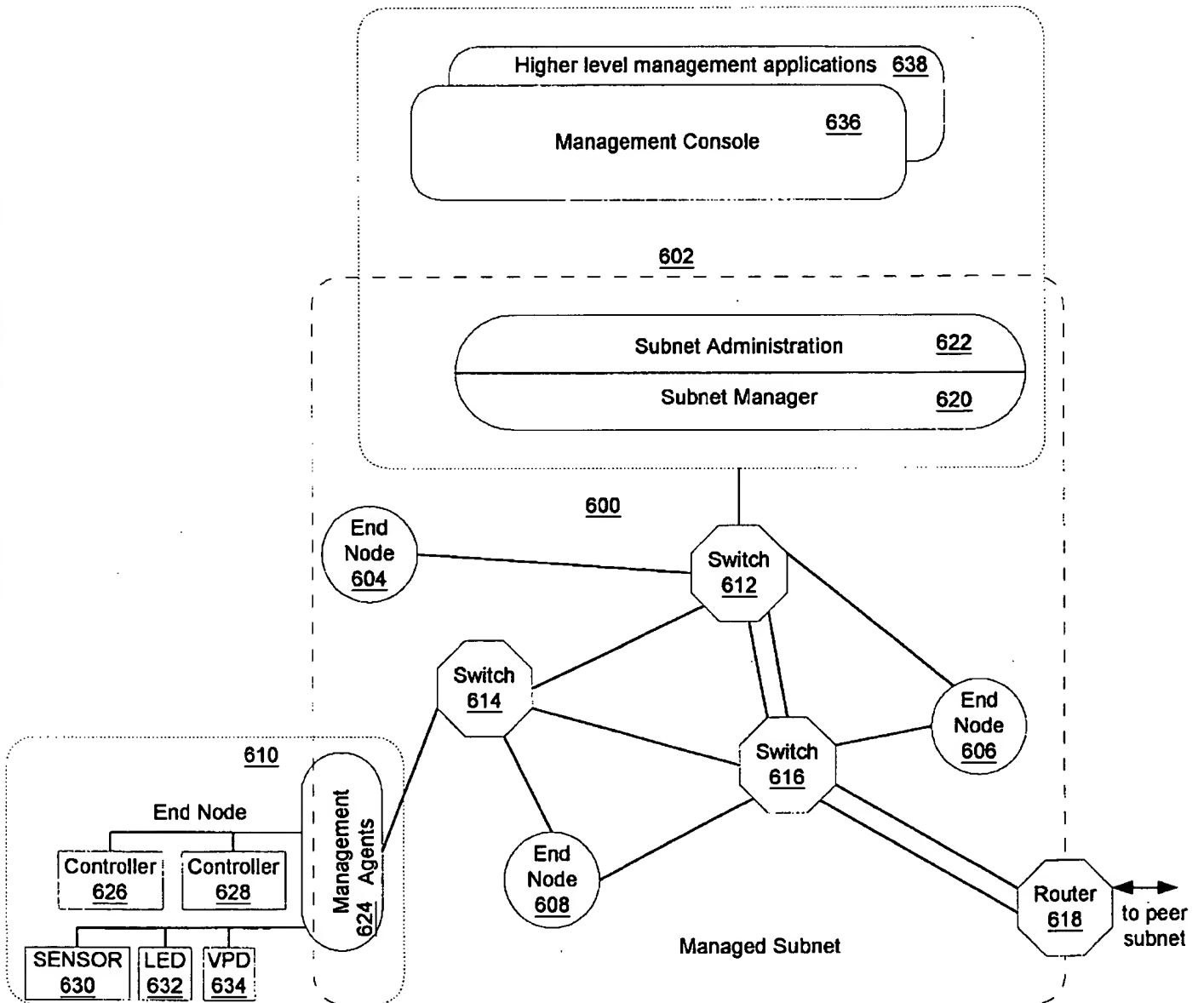
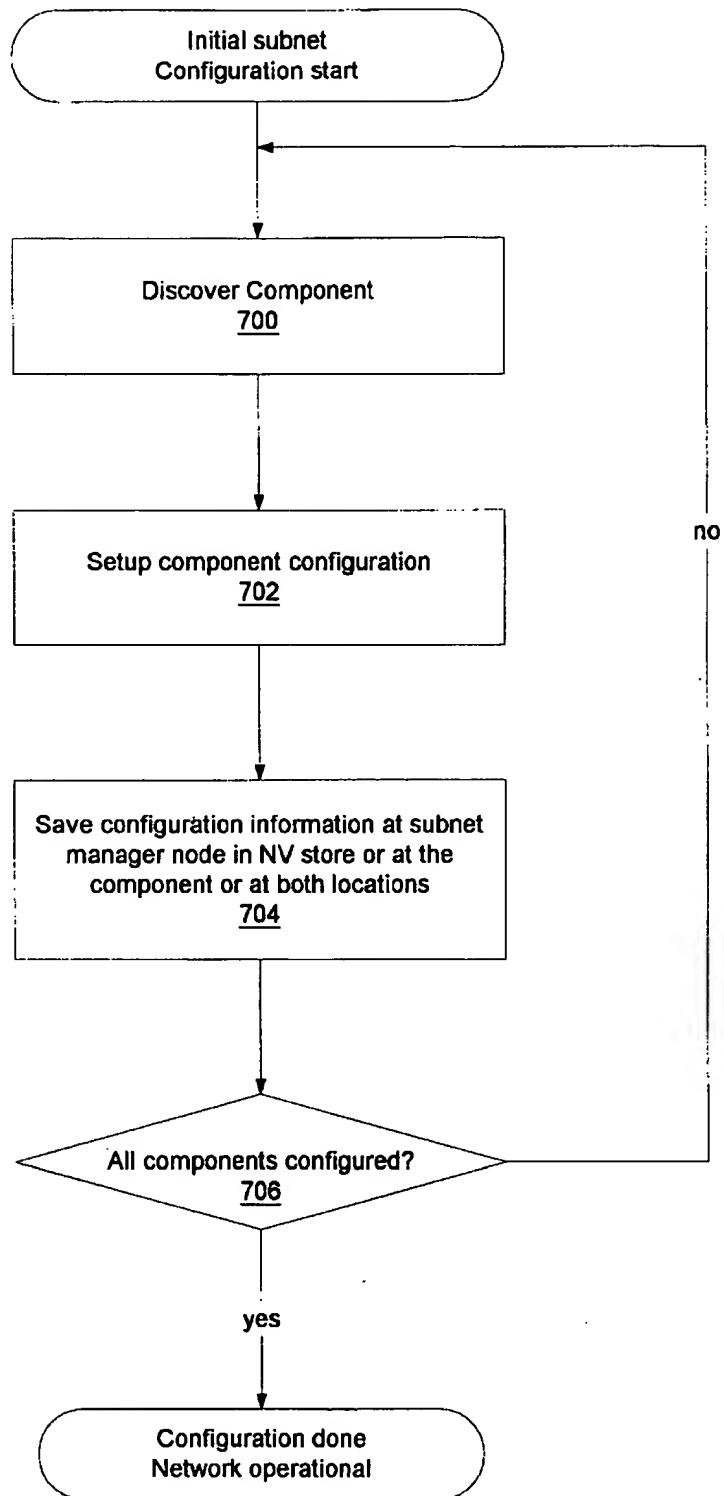


Figure 7

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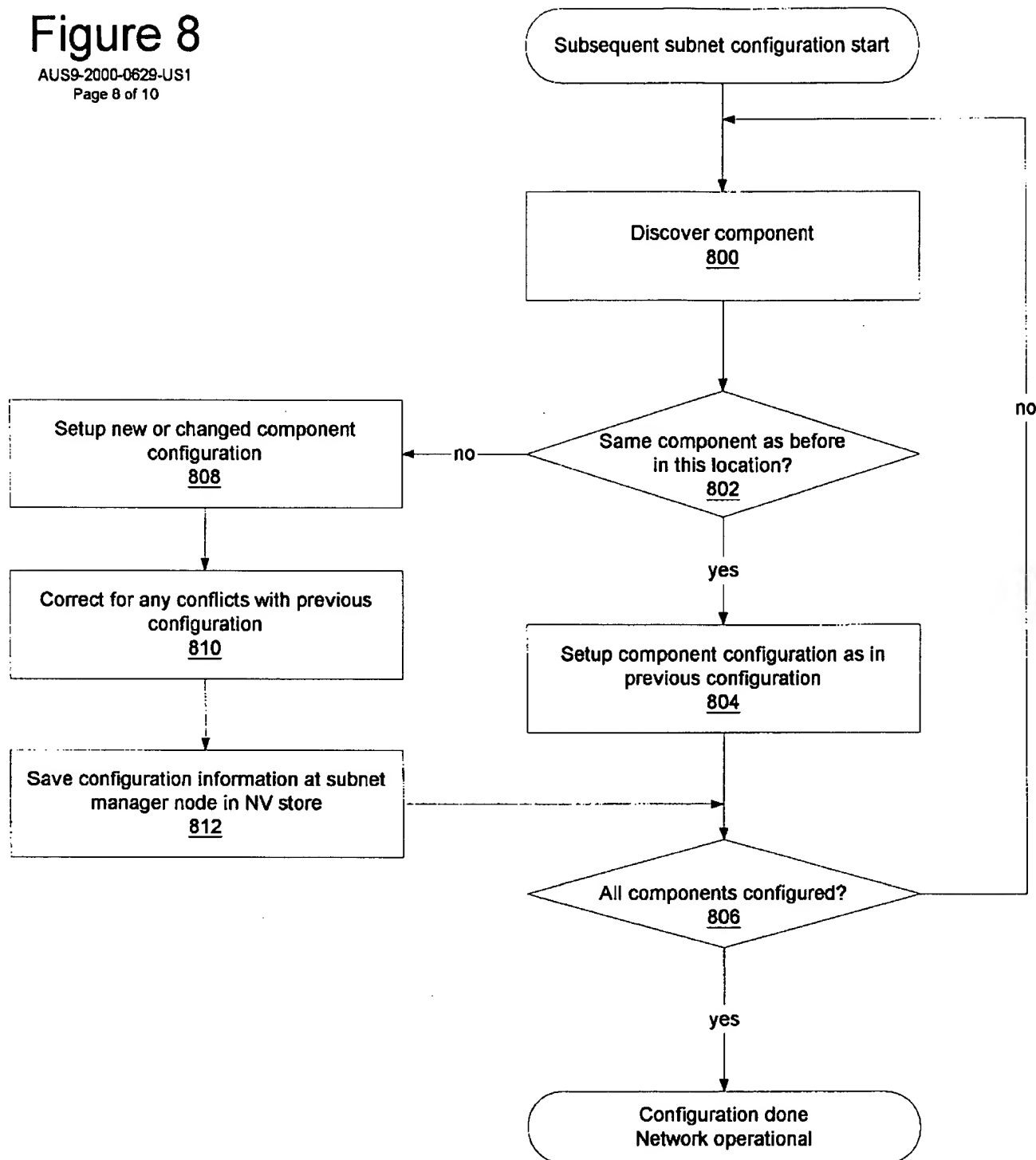


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Figure 8

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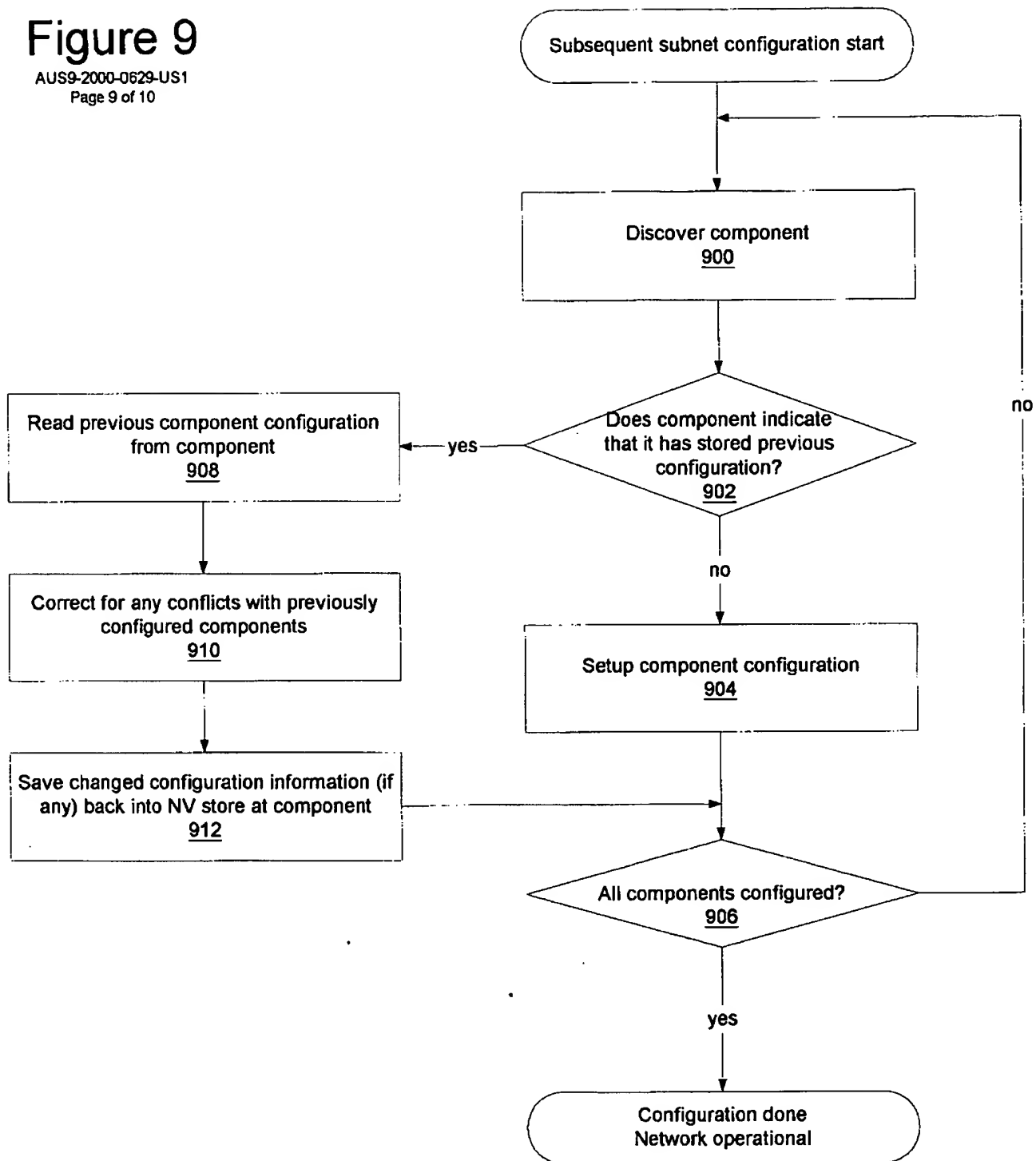


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Figure 9

AUS9-2000-0629-US1

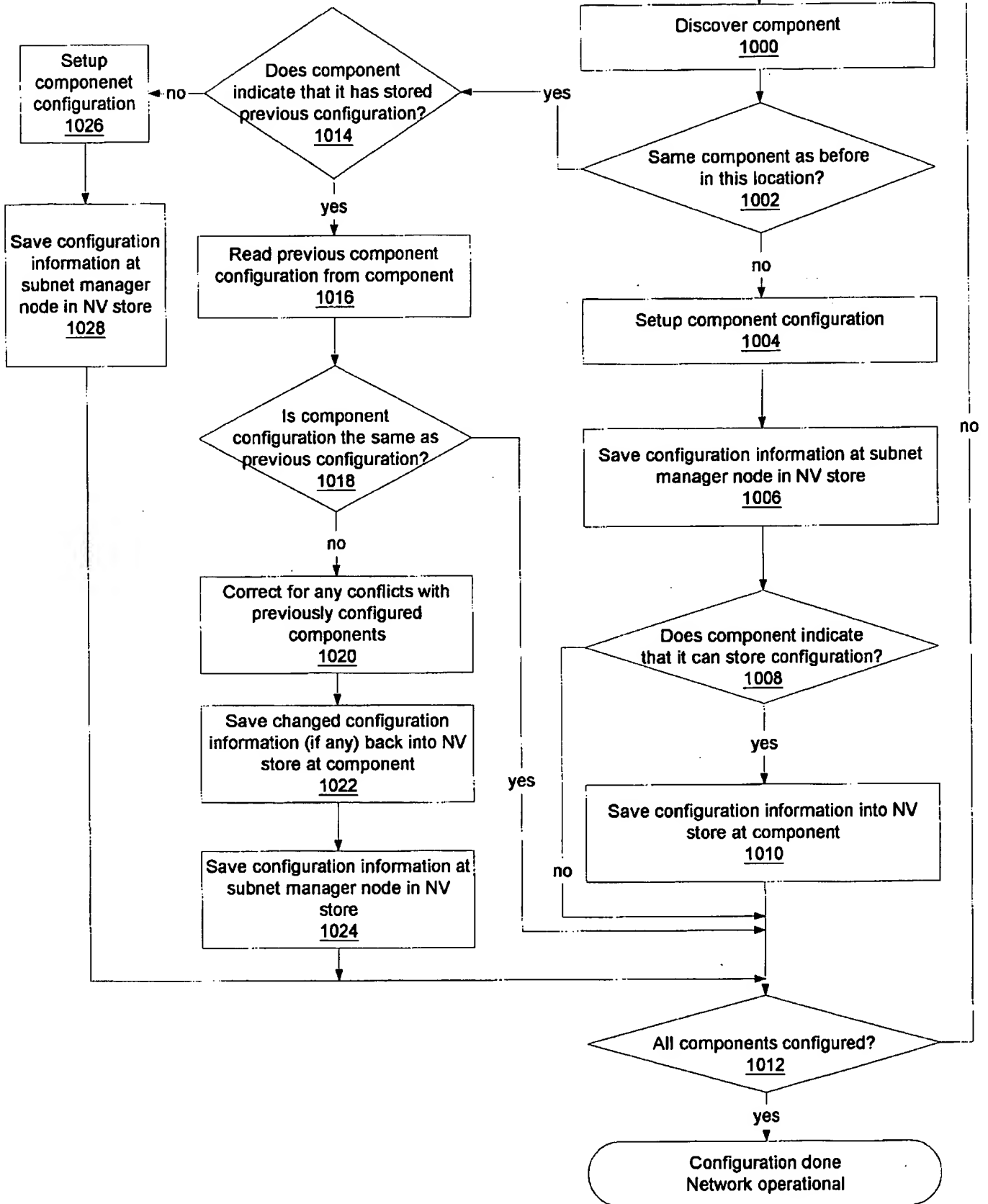
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Figure 10

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